

**REMARKS**

Claims 35-75 are pending. By this Submission, claim 47 is amended and claims 59-75 are added. No new matter is added by any of these amendments.

Applicant appreciates the courtesies extended to Applicant's representative by Examiner Liang during the February 10, 2004 interview. In accordance with MPEP §713.04, the points discussed during the interview are incorporated in the remarks below and constitute Applicant's record of the interview.

Applicant gratefully acknowledges that the Final Office Action indicates that claims 35-46 and 49-58 are allowed. However, Applicant asserts that claims 47, 48 and 59-75 are also allowable for the reasons discussed below.

Reconsideration based on the following remarks is respectfully requested.

**I. Claims 47 & 48 Satisfy the Requirements under 35 U.S.C. §112, first paragraph**

The Final Office Action rejects claims 47 and 48 under 35 U.S.C. §112, first paragraph, as being nonenabling. Claim 47 has been amended to obviate this rejection by reciting features supported in the specification on page 10, lines 14-19 and Fig. 7 in an exemplary embodiment. Withdrawal of the rejection under 35 U.S.C. §112, first paragraph is respectfully requested.

**II. Claims 47 and 48 Define Patentable Subject Matter**

The Final Office Action rejects claims 47 and 48 under 35 U.S.C. §103(a) over European Patent EP 0 895 219 to Ozawa *et al.* (Ozawa) in view of U.S. Patent 3,443,151 to Myers *et al.* (Myers). This rejection is respectfully traversed.

The combination of Ozawa and Myers does not teach or suggest a driver circuit for a current driven element, including a storage capacitor; a driving transistor of which a gate is connected to the storage capacitor; an n-channel transistor, and a p-channel transistor, an operating voltage of the driving transistor being set by the storage capacitor by flowing a data

current according to a data signal, a driving current that flows through the current driven element flowing through the n-channel transistor, the p-channel transistor and the driving transistor, and the current driven element being disposed between the n-channel transistor and the p-channel transistor, as recited in claim 47.

Instead, Ozawa discloses a display device with TFTs 20, 30. In particular, Ozawa teaches the n-channel TFT 30 having a gate connected to the driving transistor 20. See paragraphs [0074] and [0075] and Fig. 15 of Ozawa.

Myers does not compensate for the deficiencies of Ozawa. Instead, Myers discloses a control circuit 10. In particular, Myers teaches field effect transistors 20, 26 in series, and an electroluminescent cell 16. See col. 2, lines 63-71 and Fig. 1 of Myers.

Further, there is no motivation to combine features related to device driver of Ozawa with control circuit of Myers, nor has the Final Office Action established sufficient motivation or a *prima facie* case of obviousness.

For at least these reasons, Applicant respectfully asserts that the rejected independent claim 47 is now patentable over the applied references. The rejected dependent claim 48 is likewise patentable over the applied references for at least the reasons discussed as well as for the additional features it recites. Additionally, new claims 59-75 and are also allowable. Consequently, all the claims are in condition for allowance. Thus, Applicant respectfully requests that the rejection under 35 U.S.C. §103 be withdrawn.

### **III. Conclusion**

In view of the foregoing amendments and remarks, Applicant respectfully submits that this application is in condition for allowance. Favorable consideration and prompt allowance are earnestly solicited.

Should the Examiner believe that anything further is desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact Applicant's undersigned representative at the telephone number listed below.

Respectfully submitted,



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